

104
437-193

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5342806

SERIAL NUMBER (Series of 1987)	PATENT DATE		PATENT NUMBER	
11/780455	AUG 30 1994		5342806	
SERIAL NUMBER	FILING DATE	CLASS	SUBCLASS	GROUP ART UNIT
		437	195 12	1107
			61	
EXAMINER TSAI				

APPLICANTS

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H.A.T.

SEARCHED/PAT. APPLICATIONS EXAMINED

SEARCHED	12/21/87	1
JAPAN	12/19/87	1
FRANCE	12/20/87	1
UK	12/21/87	1
GERMANY	12/21/87	1

Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED →	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP. CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO.
Verified and Acknowledged Examiner's Initials H.A.T.			TOY	10	20	2	\$470.00	10-001450

ADDRESS
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TITLE
Method for fabricating A
SEMICONDUCTOR DEVICE Having A Conductor Structure with A plated layer

U.S. DEPT. OF COMM., PAT. & TM OFFICE - PTO-436L (Rev. 10-78)

ISSUE FEE IN FILE

PARTS OF APPLICATION FILED SEPARATELY		
NOTICE OF ALLOWANCE MAILED		PREPARED FOR ISSUE 7-27-93
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ISSUE FEE		Olik Chaudhuri Supervisory Patent Examiner Patent Examining Group 110 OC Primary Examiner
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PATENT NUMBER	ORIGINAL CLASSIFICATION				
	CLASS	SUBCLASS			
	437	195			
APPLICATION SERIAL NUMBER	CROSS REFERENCE(S)				
07/780455	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
	437	198	200	230	247
APPLICANT'S NAME (PLEASE PRINT)	ASAHI NA				
IF REISSUE, ORIGINAL PATENT NUMBER					
INTERNATIONAL CLASSIFICATION (INT. CL. 45)					
H 01 L	21/44				
H 01 L	21/48				
GROUP ART UNIT	ASSISTANT EXAMINER (PLEASE STAMP OR PRINT FULL NAME)				
1107	H. JEE TSAI				
	PRIMARY EXAMINER (PLEASE STAMP OR PRINT FULL NAME)				
	O. CHAUDHURI				

ISSUE CLASSIFICATION SLIP

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